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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10069522	FILING DATE 05/29/2002	CLASS 324	SUBCLASS 523	GAU 2862	LA.R <i>A.LC</i>	EXAMINER
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**APPLICANTS: Ishioka Shogo; Yamaoka Shuji; 2856

**CONTINUING DATA VERIFIED:

THIS APPLICATION IS A 371 OF PCT/JP01/04994 06/13/2001 *DJ*

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** FOREIGN APPLICATIONS VERIFIED: *DJ*

JAPAN 2000-182118 06/16/2000

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met		<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO 020239
Verified and Acknowledged Examiner's initials <i>Donald M. Fair</i>			
TITLE : Device and method for inspecting circuit board			

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L(Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drawg.	Figs. Drawg.	Print Fig.
TERMINAL		Application Examiner		
DISCLAIMER		PREPARED FOR ISSUE		
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